





Manufacturing Methods & Technology Technical Report

ECOM-77-0526-1

MANUFACTURING METHODS AND TECHNOLOGY FOR TAPE CHIP CARRIER

William R. Rodrigues de Miranda Honeywell Inc. St. Petersburg, Florida 33733

NOVEMBER 1977

Quarterly Report for Period Ending 30 September 1977

(Approved for Public Release - Distribution Unlimited)

PREPARED FOR

ECOM

US ARMY ELECTRONICS COMMAND FORT MONMOUTH, NEW JERSEY 07703

CONTRACT NO. DAAB07-77-C-0526 HONEYWELL INC. St. Petersburg, Florida 33733

Preceding Page BLANK - FILMED

Manufacturing Methods and Technology Technical Report ECOM 77-0526-1	
MANUFACTURING METHODS AND TECHNOLOGY ENGINEERING FOR TAPE CHIP CARRIES	OGY R.
William R. Rodrigues de Miranda Honeywell Inc. V St. Petersburg, FL 33733	a)
11 Nov 77	12) 49p.
Quarterly Report for Period Ending 30 Sept (Approved for Public Release - Distribution	
Prepared for U.S. Army Electronics Co Ft. Monmouth, NJ	ommand
Contract # DAAB 07-77-C-0526	ACCESSION for
	NTIS DDC B fil Sexten UNANNOUNCED JUSTIFICATION BY
Honeywell, She, AVIONICS DIVISION ST. PETERSBURG, FLORIDA 393 215	DISTRIBUTION/AVAIT ABILITY COTES DIST. SP CIAL
;;;	nt

DAAB 07-77-C-0626
QUARTERLY TECHNICAL REPORT FOR PERIOD ENDING 30 SEPTEMBER 1977
MANUFACTURING METHODS AND TECHNOLOGY ENGINEERING FOR
TAPE CHIP CARRIER

CDRL C-002

V

Treceding tage blann filmed

TABLE OF CONTENTS

		Page							
List of Illustrations Introduction									
Section	<u>1</u>								
1	EQUIPMENT	1-1							
	A Thick Film Equipment B Reel-to-Reel Plater C Automatic Wire Bonder D Outer Lead Bonder - Framer E Outer Lead Bonder - Tester/Coder F Outer Lead Bonding - Composer G Outer Lead Bonder - Assembler H Die Placement and Epoxy Dispenser I Burn-In Equipment J Tape Inspection Equipment K Inner Lead Bonder L Laser Equipment M Capacitor Pick and Place N Wafer Inspection Equipment O Automatic Test Equipment								
2	FACILITY								
3	MATERIAL HANDLING								
4	CONVENTIONAL PROCESS DEVELOPMENT								
	A Thick Film B Capacitor Attach C Inductor Placement D Automatic Wire Bond Tests	4-1 4-1 4-1 4-1							
5	TCC PROCESS DEVELOPMENT	5-1							
	A Standard Lead Frame Design and Bonding Patterns B Massbond 4810 Outer Lead Bonder C Wafer Bumping	5-1 5-1 5-8							

Preceding Page BLANK - FILMED

TABLE OF CONTENTS (Continued)

Section			Page
6	DESIG	N, LAYOUT AND MANUFACTURE OF ENGINEERING SAMPLES	6-1
	A E	Electronic Commutator (34030402)	6-1
	B F	Random Access Memory (34030405)	6-5
	c s	SINCGARS Discriminator (34030408)	6-5
	D M	Minilaser Counter (3403041)	6-9
	E C	Crystal Oscillator (34030418)	6-9
	FI	Cemperature Controller (34030415)	6-9
	M	Manufacturing Schedule of First Lot Engineering Samples	6-9
		Conclusions	6-20
		Plans for Next Reporting Period	6-20
		Publications	6-20
	DISTE	RIBITION LIST	D-1

LIST OF ILLUSTRATIONS

Figure	<u>Title</u>	Page
1-1	Weltek Model 44 Screen Printer with Honeywell Substrate Feed Mechanism	1-2
2-1	Preliminary Process Flow Chart	2-2
3-1	Substrate Magazine	3-2
3-2	Mechanical Automatic Substrate Handler	3-4
3-3	Mechanical Automatic Handling System Adapted to Weltek Printhead	3-6
3-4	Air Automatic Substrate Handler	3-7
5-1	Standard Lead Forming Configuration (Small)	5-2
5-2	Standard Lead Forming Configuration (Medium)	5-3
5-3	Standard Lead Forming Configuration (Large)	5-4
5-4	Small Outer Bond (SOB) Pattern	5-5
5-5	Medium Outer Bond (MOB) Pattern	5-6
5-6	Large Outer Bond (LOB) Pattern	5-7
6-1	Electronic Commutator Schematic	6-2
6-2	Electronic Commutator Circuit Layout	6-3
6-3	Electronic Commutator Parts List	6-4
6-4	RAM Schematic	6-6
6-5	RAM Circuit Layout	6-7
6-6	RAM Parts List	6-8
6-7	SINCGARS Discriminator Schematic	6-10
6-8	SINCGARS Discriminator Circuit Layout	6-11
6-9	SINCGARS Discriminator Parts List	6-12
6-10	Minilaser Counter Schematic	6-13
6-11	Minilaser Circuit Layout	6-14
6-12	Minilaser Parts List	6-15
6-13	Electronic Commutator Schedule	6-16
6-14	RAM Schedule	6-17
6-15	SINCGARS Discriminator Schedule	6-18
6-16	Minilager Counter Schedule	6-19

Preceding Page BLANK - FILMED

INTRODUCTION

The purpose of this program is to demonstrate the concept of an automated assembly line for hybrid microcircuits, through the establishment of techniques for tape carrier mounting of semiconductor chips, burn-in and testing of these chips on tape and their placement into representative hybrid circuits. The Tape Chip Carrier (TCC) system permits mounting of semiconductor chips on reels of sprocketed film. The system is an established means of automating the interconnection of individually packaged semiconductor chip devices. It has been adapted to the fabrication of hybrid microcircuits used in the manufacture of certain commercial computers. Its overall adaptation to the hybrid microcircuit industry is expected to be greatly enhanced by this program. The automated assembly line will make use of an automatic feed mechanism at each process step, and magazines to transport substrates and partially assembled circuits between process points.

This is the first quarterly report on the MM&T program. At this important milestone, Honeywell is pleased to report excellent progress in all areas of activity.

EQUIPMENT

This task covers the evaluation of existing, in-house equipment for possible use for the MM&T tasks to be performed, and the review of requirements and preparation of specification for equipment to be purchased. Each of the pieces of major equipment required for the program are listed on the PERT chart submitted as CDRL - A001 in August 1977, and will be further discussed below.

A. THICK FILM EQUIPMENT

After review of a number of different candidates the Weltek Model 44 printhead was selected as the most suitable for use on the automated line. This decision was based on: 1) the ease of adaptability to the Honeywell designed feed system (See Section III), 2) the good quality and simplicity of this printer and 3) the fact that this facility has an automatic printing and drying system on order from Weltek for another program, and which uses the same printhead. This printer will be the first of the manufacturing equipment on which the Honeywell automatic feed system will be interfaced and tried out (see Figure 1-1).

B. REEL-TO-REEL PLATER

It has been decided not to perform reel to reel gold plating of lead frame tape in house as several vendors are available who have both equipment and experience to do this job. One of the vendors, Reliable Electronic Finishing Co. of Canton, MA has been visited. All vendors have submitted quotes. Vendor selection will be made during the next reporting period.

C. AUTOMATIC WIRE BONDER

One K&S Model 1413 Automatic Wire Bonder is currently being used in this facility's regular production line. Valuable production experience is being gained from this equipment. A second bonder, to be dedicated to the ECOM line, will be specified in December and ordered in January 1978. This bonder will have the automatic pattern recognition feature.

D. OUTER LEAD BONDER - FRAMER

A meeting was held at Honeywell with personnel of the Jade Corporation in order to initiate detail definition of the configurations of the

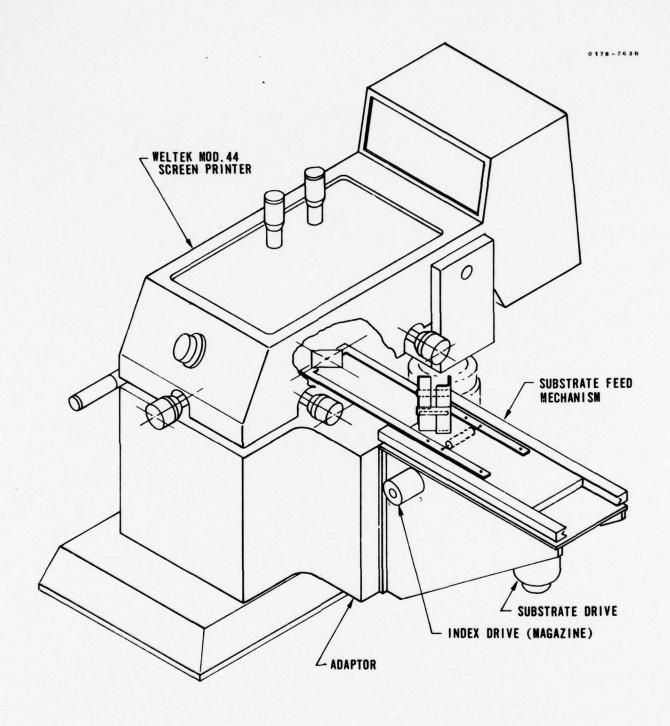


FIGURE 1-1. WELTEK MODEL 44 SCREEN PRINTER WITH HONEYWELL SUBSTRATE FEED MECHANISM

Outer Lead Bonding and Tape Testing Equipment. A tentative decision was made to provide tape coding capability by adding a strip of magnetic tape to the 35 mm carrier frame. Also it was decided to review the possibility of having the carrier frame fit a smaller size tape (e.g., 19 mm) as well. The Jade Massbond 4810 manual integrated Outer Lead Bonder was delivered and installed at Honeywell. Operation of this equipment will play a key role in establishing the detail requirements for the automatic system.

Jade will start the initial design phase of the equipment. Final specifications will be written during December and January. Equipment will be ordered in the January-February 1978 time frame.

E. OUTER LEAD BONDER - TESTER/CODER

See Item D, Outer Lead Bonder - Framer

F. OUTER LEAD BONDING - COMPOSER

See Item D, Outer Lead Bonder - Framer

G. OUTER LEAD BONDER - ASSEMBLER

See Item D, Outer Lead Bonder - Framer

H. DIE PLACEMENT AND EPOXY DISPENSER

A visit was made to K&S in September 1977 to obtain information on design concepts by K&S. A machine (Model 621) for this purpose was on the drawing board at that time. A visit of K&S to Honeywell has been scheduled for late November, in order to further define the equipment and to prepare specifications. The equipment will be ordered in January 1978.

I. BURN-IN EQUIPMENT

Initial design concepts of the burn-in equipment have been completed. The current approach will make use of the tape deposited into 35 mm carriers in individual frames. A tray has been designed which will provide electrical contact to about 100 carriers. A temperature control system will be designed which can accept up to 10 trays. Final equipment specifications will be completed in February 1978 as currently scheduled on the PERT chart. Equipment will be manufactured in-house.

J. TAPE INSPECTION EQUIPMENT

After a careful review it was decided that no special tape inspection equipment, other than that already in-house, may not be required. This equipment consists of manual spooler/despooler with a Bausch & Lomb Model Stereozoom 7 microscope set-up. Further review of this equipment, now scheduled for December, will determine if further modifications are required. If such is the case, the modifications will be made in-house.

K. INNER LEAD BONDER

The Inner Lead Bonder will be much like the standard equipment available at Jade. The few minor special requirements will be specified during December and January. The bonder will be ordered in March 1978 as shown in the PERT chart.

L. LASER EQUIPMENT

No activity was scheduled during this reporting period for modifications of the automatic laser equipment to mark reject chips. Specifications activity is scheduled for February 1978.

M. CAPACITOR PICK AND PLACE

Extensive work has been accomplished on the Browne Model CP-10 capacitor placement equipment now operational in the facility. Additional evaluation will continue during the month of December in preparation for modifications to adapt the automatic feed system.

N. WAFER INSPECTION EQUIPMENT

The requirements for wafer inspection equipment have undergone an initial review. There is a possibility that no special modifications to existing equipment (Nikon Model 71772) needs to be made. Review will continue and a decision will be made at the end of December 1977.

O. AUTOMATIC TEST EQUIPMENT

Existing automatic test equipment (Fairchild 5000, Hewlett-Packard 3042A and Macrodata 150) will be used to test the sample hybrids. Programs are now being written for the first lot of engineering samples. Modifications will be made as necessary during the next reporting period.

FACILITY

Facility plans are only in the preliminary stage with the determination of the space requirements for the new equipment items. The process flow has been largely determined and a preliminary diagram is shown on Figure 2-1. More definite plans will be prepared during the next reporting period.

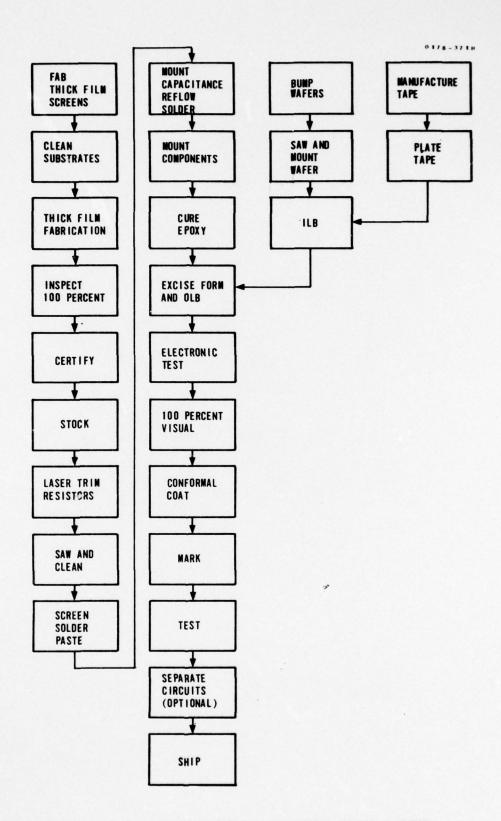


FIGURE 2-1. PRELIMINARY PROCESS FLOW CHART

MATERIAL HANDLING

A key objective of the MM&T program is to develop automatic handling techniques and equipment for the hybrid substrates.

The substrate size has been standardized a 2" square by .025" - .040" thick. A typical substrate is processed thru (9) different work stations;

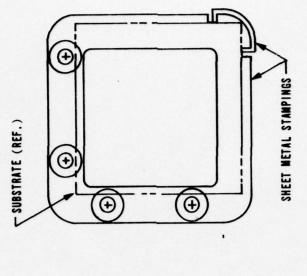
- 1. Thick Film Printer
- 2. Thick Film Dryer
- 3. Thick Film Furnance
- 4. Laser Trim (Resistors)
- 5. Pick and Place (capacitors)
- 6. Solder Reflow
- 7. Epoxy Dispense and Die Place
- 8. Outer Lead Bond
- 9. Auto Wire Bond.

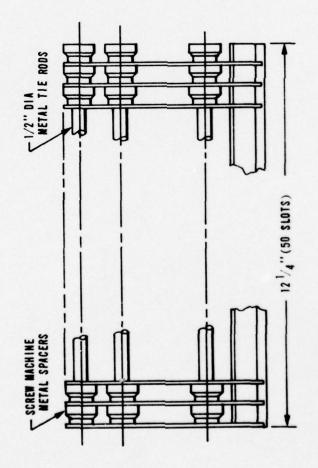
The thick film printing, drying and firing operations are repeated an average of 10 times on each substrate, thus the typical substrate is handled approximatley 40 times.

The substrates will be handled manually only at the initial loading into a 50 slot magazine. (See Figure 3-1) Thereafter they will be automatically fed from the magazine to the work station, processed, and fed back into the magazine. Magazines will be transported manually.

To meet substrate quality, cost and production rate requirements the following groundrules were established for the handling equipment:

- The same magazines will be used for all handling and for "dry box" storage.
- 2. The magazines will keep individual substrates physically separated.
- The automatic feed mechanisms will be basically similar for all work stations.





SCALE: FULL

FIGURE 3-1. SUBSTRATE MAGAZINE

- 4. The automatic feed cycle moving the substrate from magazine to work station, back into the magazine & indexing to the next substrate, will be accomplished in 1.5 secs. maximum.
- 5. In all operations, location will be against the same three points on the same two edges of the substrate.
- 6. Substrate positioning accuracy on the various work stations will be repeatable to within .0015".
- 7. Automatic feeding will be continuous there will be no "break" between magazines.

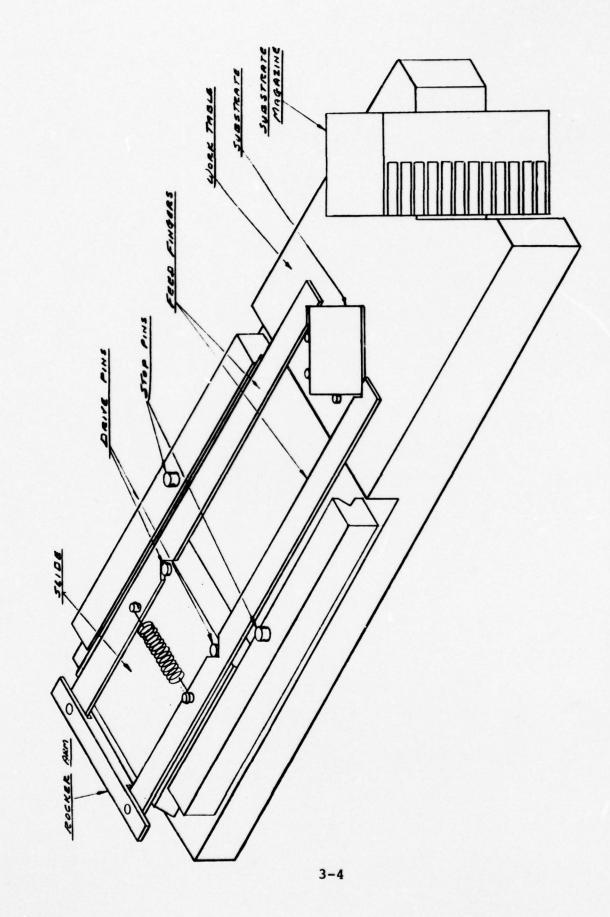
It was decided to design & build the automatic handling equipment in-house for the following reasons:

- 1. Very little of this type equipment is commercially available. What is available is the general purpose type that would require considerable modification to meet our requirements. None of it is shelf hardware. It would have to be built to order.
- 2. The automatic handling equipment must be adapted to (9) different work stations, some of which are still in the development stage. In-house design and build of the handling equipment greatly simplifies the coordination and scheduling problems.
- 3. We have the in-house design and build capabilities.

We are developing, in parallel, two basically different types of automatic handling equipment; A mechanical handler and an air handler.

The mechanical handler employs feed fingers driven by a programmed step motor to transport and position substrates. It differs from conventional feed mechanisms in three basic respects: (See Figure 3-2)

- 1. The fingers grip the substrate by opposing corners. This eliminates interference between fingers and work table locating pins and allows use of the fixed locating pins. Inherently the most accurate approach. As the feed fingers move past the substrate seating point, they ride up the edges of the substrate and hold it under spring tension against the locating pins. This eliminates the conventional additional "snugger" mechanism.
- 2. The feed fingers are driven thru "V" grooves spring loaded against drive pins. When return travel of the fingers is halted by the stop pins, continued travel of the drive pins opens the fingers, releasing the substrate and allowing clearance for the magazine to index. Stop pins (not shown) are added at the other end of the travel to open the fingers at the work table when the table must be moved during substrate processing.



T

FIGURE 3-2. MECHANICAL AUTOMATIC SUBSTRATE HANDLER

3. The rear ends of the feed fingers are pivoted in a rocker arm. This, combined with the "V" groove drive, allows the substrate a limited "float" both in rotation and at right angles to the direction of travel, thus eliminating locating errors due to substrate dimensional variations.

We have a high confidence level in the mechanical approach since it uses proven techniques. The first mechanical handler will be adapted to the Weltek thick screen machine. (See Figure 3-3).

The alternative air handler (See Figure 3-4) employs directed air jets to transport and position substrates. It offers significant advantages in cost, size, and reliability over the mechanical handler. Short stroke feed fingers move the substrate from the magazine onto the air slide. A series of air jets slanted at 45° to the direction of travel and directed against the bottom of the substrate move it to the work table and against the (3) locating points. The substrate is held in position during processing by vacuum.

After processing it is moved back to the magazine by a second series of air jets slanted in the opposite direction. Inertia carries it into the magazines.

A feasibility model has been built which demonstrates that air jets will transport and position substrates with the required speed, accuracy and safety. However the air handler uses several unproven techniques which need additional development.

We expect that, by 2/1/78, either the mechanical or the air handler will have demonstrated its superiority and the other handler will have been dropped. However, the mechanical handler could prove best for some applications, the air handler best for others.

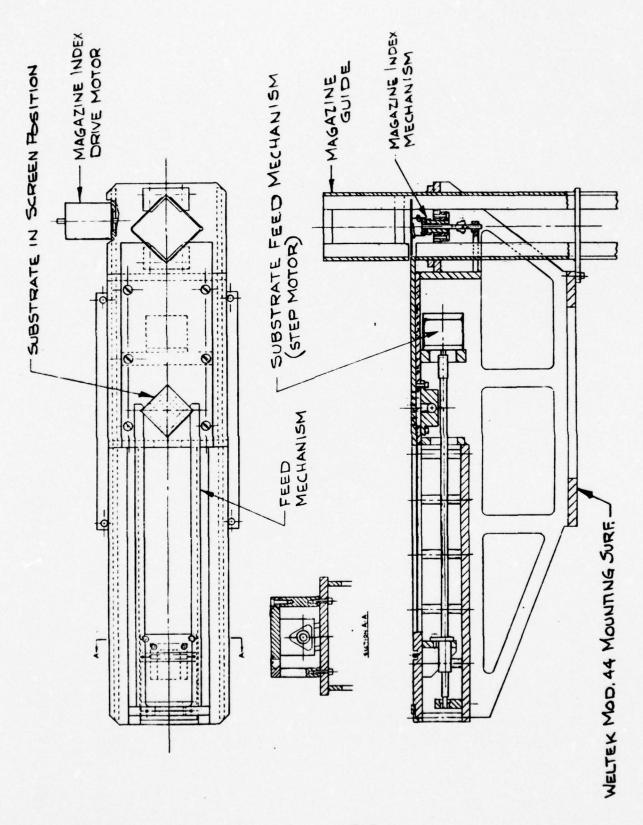


FIGURE 3-3. MECHANICAL AUTOMATIC HANDLING SYSTEM ADAPTED TO WELTEK PRINTHEAD

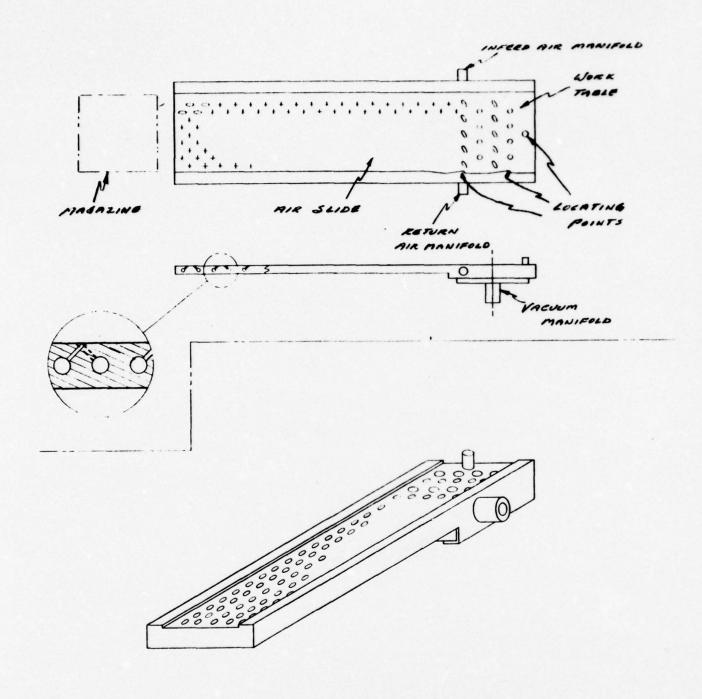


FIGURE 3-4. AIR AUTOMATIC SUBSTRATE HANDLER

CONVENTIONAL PROCESS DEVELOPMENT

A number of activities in the area of conventional process development were planned on the PERT chart. With some of the circuit changes and early decisions in technical approaches, several of these efforts were redirected. The following is a brief summary of the work performed to date.

A. THICK FILM

A number of thick film printers available to this facility were evaluated against the requirements for the automatic assembly line. Also several vendors for new equipment were contacted and discussions were held. With the proposed change from the "FM Oscillator" to the "Temperature Controller" circuit, certain unique requirements for the printer could be deleted, and a printer more suitable for automated assembly could be selected. Available automatic feed systems mechanisms were not deemed accurate enough, and therefore a decision to build this system with the best printing characteristics. (See Section 1). A source for chip inductors was found, deleting the requirement to develop inductor printing technology.

B. CAPACITOR ATTACH

Because of the large number of capacitors in one of the circuits and because the circuits are built two or three at a time on a 2x2 inch substrate, a technique has to be developed to easily provide for multiple passes through the Browne pick and place machine. Capability of this equipment is currently 10 chips (capacitors or inductors) maximum at one pass.

C. INDUCTOR PLACEMENT

Since chip inductors will be used, these items can be handled by the Browne equipment. The review of requirements for proper size, orientations etc. has been completed and the desired chips have been selected.

D. AUTOMATIC WIRE BOND TESTS

With the selection of a 2x2 inch standard substrate rather than a larger, e.g. 4x4 inch substrate, the need for special wire bond tests over large areas has been obviated. The 2x2 inch substrate has been used in the past and sufficient experience was gained such that no special tests are necessary.

TCC PROCESS DEVELOPMENT

A. STANDARD LEAD FRAME DESIGN AND BONDING PATTERNS

During this reporting period a set of standard lead frame trajectories and Outer Lead Bonding patterns was developed for use on all TCC bonded devices on either manual or automatic machines.

The purpose of this approach is to standardize the excise and forming dimensions for any chip size up to 0.250" square. Three standard dimensions are developed, corresponding to previously available tape window sizes of five and seven mm and a new window size of 10 mm. basic concept of this approach is to excise the tape as close as possible to the Kapton window and to use all the available copper lead length to allow maximum chip dimensions for that window size. The excise dimension determines the outer lead bonding pad placement. For varying chip sizes fitting in a certain window size the lead shape remains constant up to the top of the S curve. See Figures 5-1, 5-2 and 5-3. The length of the remaining horizontal trajectory may increase with decreasing chip size. The lead does not have the one mil rise next to the bump allowing a larger chip for a certain window size, because the horizontal fan-out can start three mils from the bump rather than from the rise edge. straight lead also allows for simpler tool design. The advantage of the standardization is that only three inserts are required for manual or automatic Outer Lead Bonders to cover a chip size range of 20 to 250 mils. Previously each chip size required its own excising size and lead form design and its own tooling inserts.

The bonding patterns are matched to accept leads excised and formed per Figures 5-4, 5-5 and 5-6. Prior patterns were adapted to each chip type and size used, resulting in a proliferation of bonding sizes. These standardized sizes are matched to the thick film preferred grid points, and they allow for via placement within the pattern.

The small size (Figure 5-4) measures 170 mils square and can accept chips up to 80 mils square, and up to 24 leads. The medium size pattern (Figure 5-5) measures 250 mils square and can accept chips up to 160 mils in largest dimension, and up to 40 leads. The large pattern (Figure 5-6) measures 350 mils and can accept 250 mil chips and up to 48 leads. The small pattern is designed so that, with 10 mil spacing between adjacent pads, and 50 mil spacing from the edges, exactly five patterns fit on a one inch substrate length. With the same 10 mil spacing exactly two small patterns fit in the space of one large pattern.

B. MASSBOND 4810 OUTER LEAD BONDER

The Jade Massbond 4810 manual outer lead bonder was delivered and

BEST AVAILABLE COPY

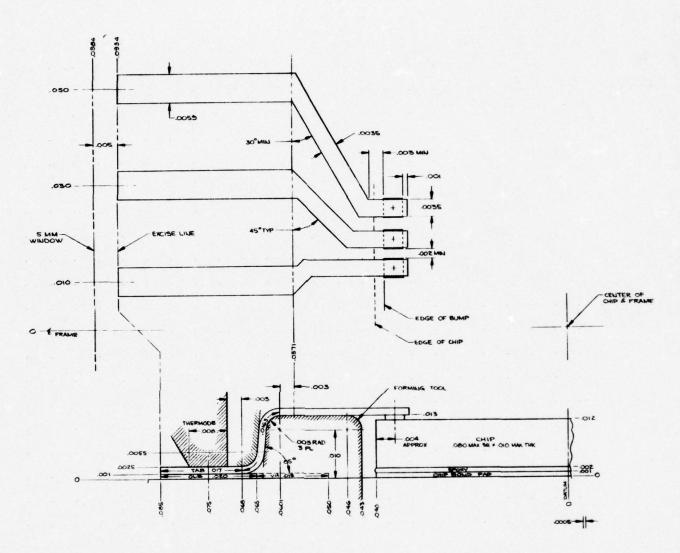


FIGURE 5-1. STANDARD LEAD FORMING CONFIGURATION (SMALL)

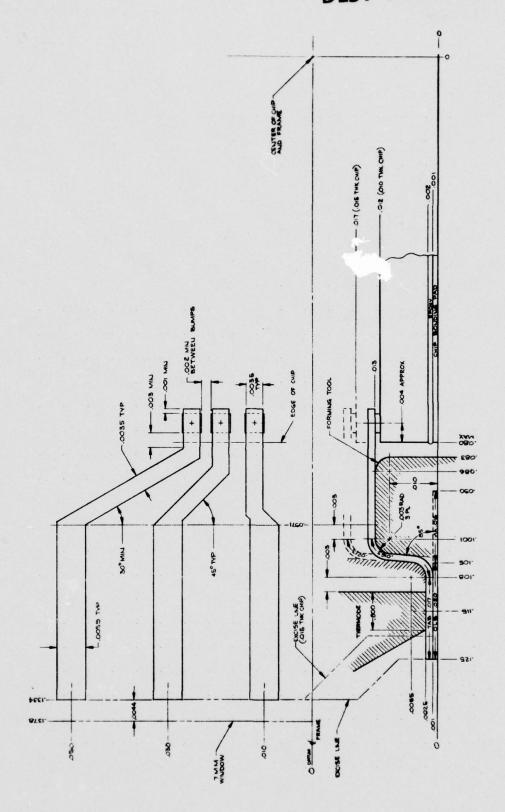


FIGURE 5-2. STANDARD LEAD FORMING CONFIGURATION (MEDIUM)

BEST AVAILABLE COPY

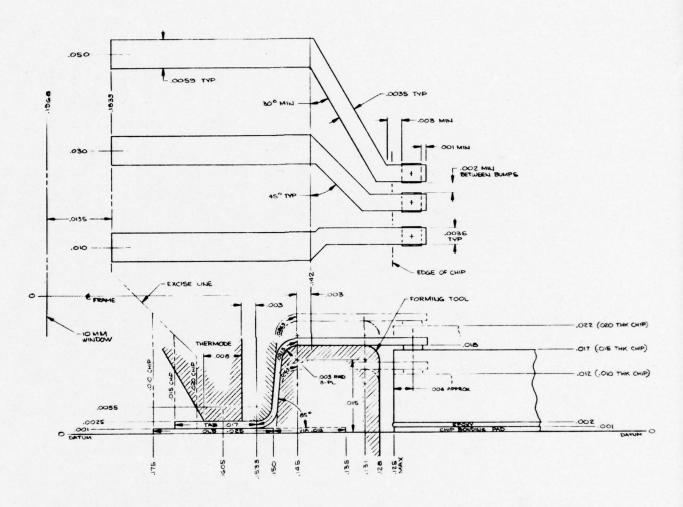


FIGURE 5-3. STANDARD LEAD FORMING CONFIGURATION (LARGE)

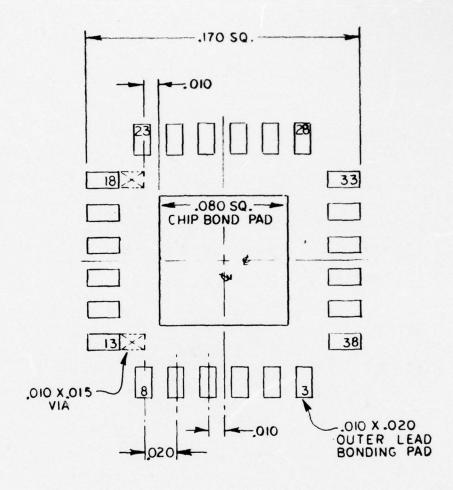


FIGURE 5-4. SMALL OUTER BOND (SOB) PATTERN

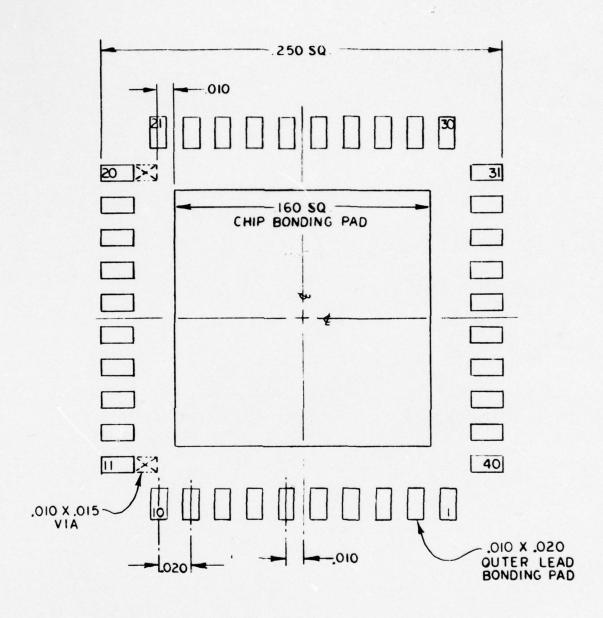


FIGURE 5-5. MEDIUM OUTER BOND (MOB) PATTERN

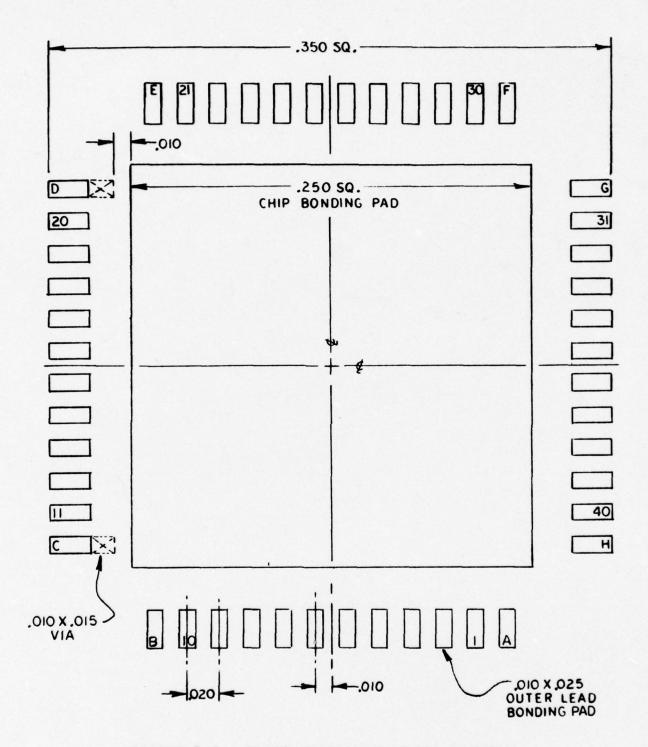


FIGURE 5-6. LARGE OUTER BOND (LOB) PATTERN

installed. This equipment is important to the development of automatic bonding capability because the excising, forming and bonding steps it performs as an integral function will be carried out in the same manner on the automatic bonder. The Jade Corporation will draw heavily on Honeywell's experience with this bonder in order to prepare the final design for the automatic bonder. Experimentation on this bonder will continue well into the next reporting period.

C. WAFER BUMPING

The Riston division of DuPont has announced that no more microelectronics films will be produced. Riston is interested in pursuing the microelectronics business which it has started; however, their approach will be to use "standard" dry-film resists. Reasons for their decision to drop the line of M-films are:

- High cost of these films (3 to 4 times higher than standard).
- Low volume production of M-films requires special order.
- Benefits of M-films over standard films are questionable.

The standard film which replaces M-811 is 211-R. This is also a red semi-aqueous, one mil thick dry film. The Riston Microelectronics Research and Development Department has assured us that no processing problems should be encountered when switching to 211-R.

No wafer bumping operations were performed during this reporting period, as the first submittal of engineering samples will be wire bonded.

DESIGN, LAYOUT AND MANUFACTURE OF ENGINEERING SAMPLES

Upon receipt of the technical specification package of the Engineering Samples a review was made of each circuit schematic and description in preparation of the layout effort. During this review it became apparent that the schematics could not be immediately translated into circuit layouts and that considerable circuit design engineering effort was required. Below follows a description of the work performed and a summary of the results.

A. ELECTRONIC COMMUTATOR (34030402)

Work performed on this circuit included the following items: (1) Timing diagram preparation and state analysis, (2) Breadboard testing of actual circuit.

A number of problems found with the Electronic Commutator are discussed below. Figure 6-1 shows the final circuit schematic. Figure 6-2 shows the layout configuration in which the device is being built. Figure 6-3 is the accompanying parts list.

- 1. A connection error on the schematic submitted in the data package is causing uneven switch times in the commutator output. Signals to U4 (CD4023) which are \overline{A} \overline{B} \overline{C} and \overline{A} \overline{B} \overline{C} and \overline{A} \overline{B} \overline{C} . In order to correct this a new layout on the conductor pattern on the substrate would be required.
- 2. A "glitch" was observed in the oscillator output during breadboard testing (U3 pin 9). It may be attributed to layout problems, i.e., long lead lengths. A 20 pf capacitor pin 11, 12, and 13 on U3 to ground corrected the problem on the breadboard. The "glitch" may yet be present on the hybrid.
- 3. The FET "off" resistance specification may not be met due to Vpd being specified at 7.5 volts. The 2N4391 guaranteed "off" voltage is Vgo (off) = -4V to -10V for $I_0 = 1 \times 10^{-9}$ amps. It is not known at this time how many devices would meet 150 ma with a gate voltage of -7.5V and a drain voltage of 15V, i.e., $15V/150 \times 10^{-9}a = 100$ M ohm which is the specification. If this is a problem, devices could be screened at the chip level to meet the specification.
- 4. The output numbering is confusing, the time sequence of switching is ..., 6, 5, 4, 3, 2, 1, 6, 5, ... etc.
- 5. To meet the switching requirement of $T_C = 0.375 \pm 0.025$ second R8 may have to be actively trimmed.

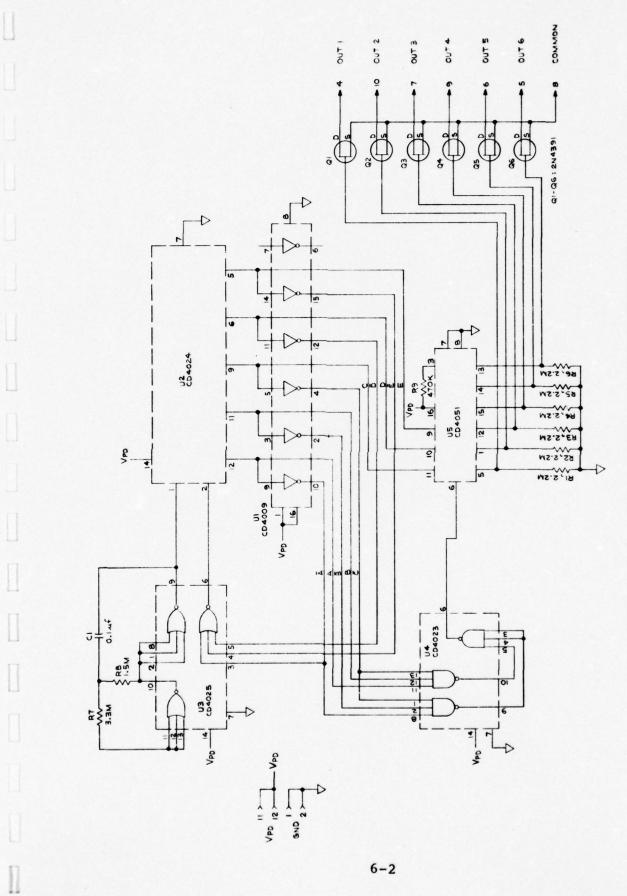


FIGURE 6-1. ELECTRONIC COMMUTATOR SCHEMATIC

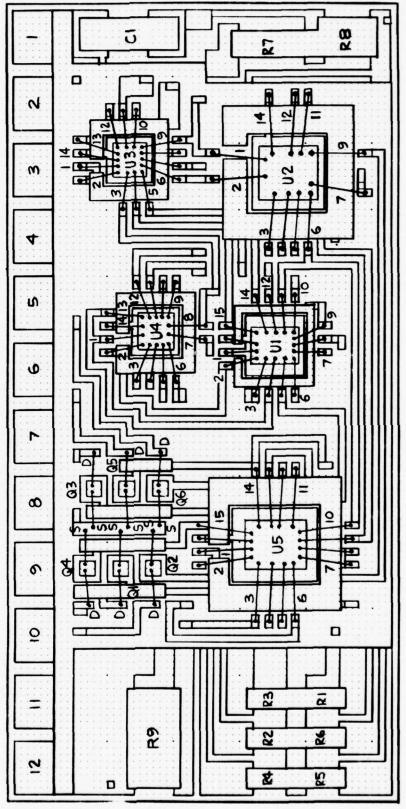


FIGURE 6-2. ELECTRONIC COMMUTATOR CIRCUIT LAYOUT

BEST AVAILABLE COPY

DATE 11-09-77	SHEET	1 DENT													09128	
REV	REV AUTH NO. E0155530	REMARKS	MIN REVISION -	Ü	01-06	71	12	J3	74	0.5	TY II ABLEBOND 36-2	.001 DIA.TY II.T.S.	62-36-2	DOW CORNING	.09 REF. HISH CHAP.	METH HS AND/OK BZ
CODE 1 DENT 09148 PL34030402-001	LIST TITLE HYP. ASSY: ELEC COMM	DESCRIPTION	MKC	V.1.6. ELEC. COMMICAF. CATE CATE			T.A.B. I.C.				ADH.		SOLDER PASTE		MARKING	TEST
AVIORICS DIVISION	COLTRACT MUNEER HIST	OTY PART NUNBER	REF 34030402	1 34030401-001 1 1505%5H104K25PS	6 AU DAN 244391	1 T.A.h. CP4009	1 T.A.9. Ch4024	1 T.A.B. CF4025	1 T.A.P. C04023	1 T.A. 4. CD4051	AP F-540599	AR FN540616			DEF FPS13032	REF 34030403
PARTS LIST	CO: TR.	NO ON	~	-1~		4	2	9	-	œ	6	. 10	11	12	REF	REF

FIGURE 6-3. ELECTRONIC COMMUTATOR PARTS LIST

B. RANDOM ACCESS MEMORY (34030405)

Work performed on the RAM included breadboard testing on the Microdata 150 automatic tester in order to verify the test program. No other analysis was required. There are no apparent problems with the device at this time. The testing will be quite a challenge, however. The test specification will be determined from GALPAT program modifications.

It should be noted that the memory is not the same functionally as proposed in 177-14222 but similar.

As originally proposed the 16K RAM used 16-1K AM9102B memory chips. Because of AM9102B chip availability problems, the readily available SY2114 4K chips were chosen as a suitable replacement. To achieve 16K bit density only 4 SY2114 chips are required. The major difference in the resulting memory system is the SY2114 chip has a common I/O while the AM9102B has separate input and output. Most modern memory systems are standardizing on 18 pin common I/O chips so it was reasoned that the substitution would be acceptable.

The RAM circuit schematic is shown in Figure 6-4. The hybrid layout in Figure 6-5, and the accompanying parts list in Figure 6-6.

C. SINCGARS DISCRIMINATOR (34030408)

Circuit analysis work performed on the Discriminator circuit included (1) HICAP analysis on the output active filter, and (2) a breadboard test of the actual circuit.

No particular problems are foreseen at this time. The original schematic had a wiring error and choice of components to be made, however, the wiring error has been corrected and inductance values chosen. The breadboard is operational and will be used to establish test parameter values for a test specification to be written.

The original circuit description (Figure 4 per description 2279857 B&C dated 17 August 1976) had Cll, which is the output, grounded, and had 22 nh chokes in the circuit for bypassing. Ll, L4 and L5 are used in conjunction with C4, C8 and C9 respectively. C8 and C9 are 0.01 μf caps which resonate with 22 mh at 10.7 MHz which is not a desirable condition for bypassing. It was argued that due to the small current draw of the amp/limitor chip RC bypassing could be used.

With regards the unspecified values of L2 (the quadrature coil) and L3 (the tuning coil) the commercially accepted values of 22 μh and 2.2 μs were used. (Reference RCA application note ICAN 6257 "Application of the CA3089E-IF Subsystem" by L.S. Baar.) The connection between L2 and L3 is also missing on Figure 4 and is added on the 34030408 schematic. Minor rewiring in the AC return connection of C3 was also made.

FIGURE 6-4. RAM SCHEMATIC

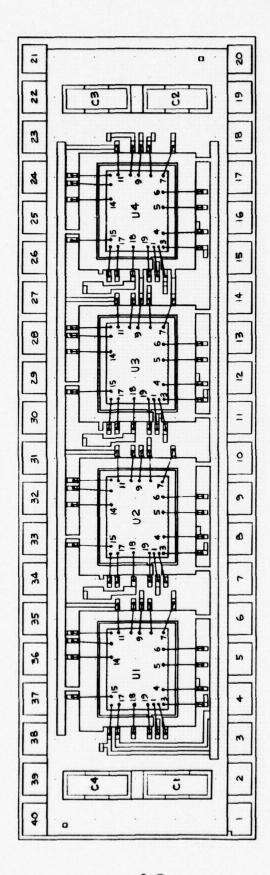


FIGURE 6-5. RAM CIRCUIT LAYOUT

SHEET	CODE		

REV AUTH NO. E0155530	REMARKS	- NOISI

DESCRIPTION

LIST TITLE HYR. ASSY. "M+T RAM

ST. DETFRSBURG FLORIDA CONTRACT SUREFR

AVIONICS DIVISION

HOME Y JELL

PARTS

HYB. ASSY. MM+T RAM W.I.B. MM+T RAM CAP. 0-1UF. 25V TAG. I.C.

34030405 34030404-001 1505.55104K25PS

7 - 4 4

PART NUMBER

FI':0

T.A.H. SY2114

DUPONT 8956 F'4540599

44

- n m 4 m 4

FPS18032

00190

34030406

REF

DATE 11-09-77

A E

PL34030405-001

CODE IVENT

87160

1

		LEBON		DOW CORNING	
		AB	-5	ORE	1
Ç	2	1	36	J	•
j	10	IY II	62-	000	

EL.CONU.EPUXY ADH. SOLDER PASTE

COAT . CUNFORMAL

SEKING.

TEST

1D 36-2

*09 REF. HIGH CHAP. METH H5 AND/OR B2

09128

RAM PARTS LIST FIGURE 6-6.

With regards the electrical requirements minor deviations in the specification for detector output and total harmonic distortion (THD) may have to be made as the choice of L2 and L3 effects these parameters.

The resulting schematic for the Discriminator is shown in Figure 6-7. The layout to which the hybrid will be built is shown in Figure 6-8, and the parts list in Figure 6-9.

D. MINILASER COUNTER (3403041)

A logic state analysis was performed on the Minilaser Counter. No problems are foreseen with this circuit at this time. The circuit schematic is shown in Figure 6-10. The hybrid layout which will be used is shown in Figure 6-11, and the parts list in Figure 6-12.

E. CRYSTAL OSCILLATOR (34030418)

The circuit analysis work to be performed on this circuit includes a (1) HICAP IIA ANALYSIS AND (2) a headboard test. Much preliminary design and review work has been completed. The performance of this circuit will be strongly dependent on the choice of piezoelectric element. The actual ceramic element, packaged as developed by ECOM will not be available nor will a complete procurement specification for an equivalent unit in a Hg-18/n case. Room on the substrate will be provided for the actual ceramic element should it become available. Some of the performance characteristics such as frequency deviation, temperature tracking of CRl with the crystal element and compensation temperature range will have to be determined after testing the original engineering units.

F. TEMPERATURE CONTROLLER (34030415)

A basic circuit analysis has been completed on this circuit, which is the temperature controller portion of the Crystal Oscillator. Yet to be performed are (1) a HICAP IID circuit analysis and (2) breadboard build and test.

MANUFACTURING SCHEDULE OF FIRST LOT ENGINEERING SAMPLES

Figures 6-13 through 6-16 show the manufacturing schedules of the Electronic Commutator, RAM, SINCGARS Discriminator and Minilaser Rangefinder circuits respectively. Manufacturing schedules for the last two circuits will be prepared as soon as the circuit definition has been approved.

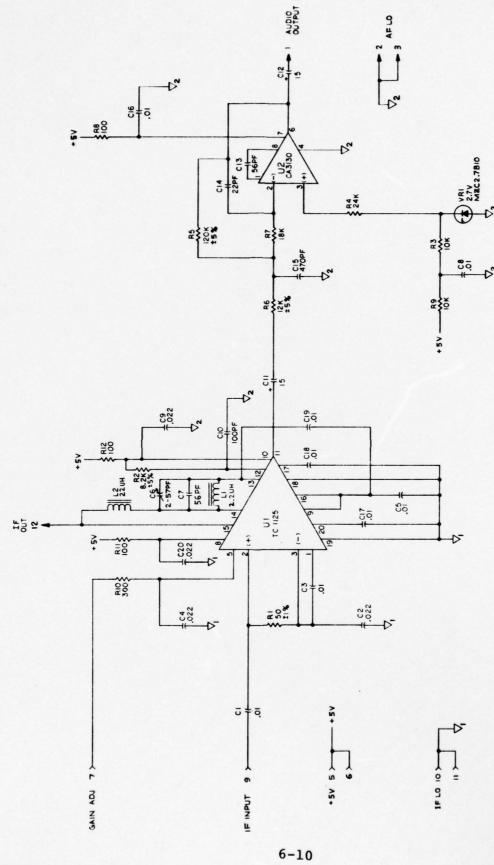
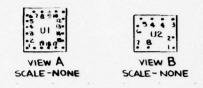


FIGURE 6-7. SINCGARS DISCRIMINATOR SCHEMATIC



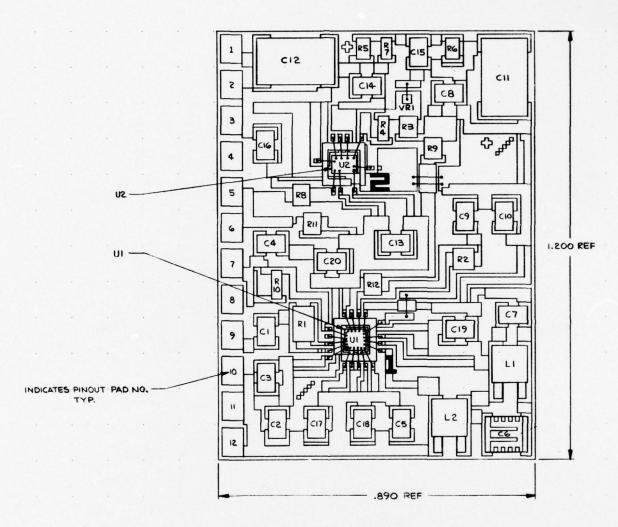


FIGURE 6-8. SINCGARS DISCRIMINATOR CIRCUIT LAYOUT

DATE 12-07-77	SHEET	CODE																							09128
REV A	PEV AUTH NO. E0155529	REMARKS	"II" - MEVISION -	VICLAH C1.3.5.8.	16-19 VICLAN C2.4.9.20	VICLAN C7.C13	VICLAN CIO	VICLAM C14	VICLAM C15	ACI C11,C12	VITCAMOL C6	VAP GUARU L1	VANGUARD L2	VR1 MOTUROLA	PCA UI	RCA U2	F/N 15, TYPE 1,	ABLEBOND 58-1	F/r. 16. TYPE II.	ABLESOND 36-2	.001 DIA.TY II.T.5.	62-36-2	DOW CORNING		METH H5 AND/OR B2
CODE ICENT 09128 PL34030408-001	TITLE ASSY. SINC. DISC.	DESCRIPTION	HYD. ASSY. SINC. DISC.	OI OF CAF	.022 UF CAP	5th PF AP	100 PF CAF	22 PF (AP	470 PF CAP	15 JF TANT CAP	2 TO 57 PF VAR. CAP	2.2 UH IMUNCTOR	22 UH INDLCTOR	DIODE	T. F. H I. C.	T.A.B.I.C.	FL.CONG. EFUXY ANH.				WIRE. Bring. GOLD	SOLDER PASTE	COAT . CONFORMAL	TEST	WARK INC.
- L >	SI. PETENSBURG FLORIDA CONTRACT NUMBER HTP.	PART GUYEER	34030408	04058401-001 04058410385005	0805%5K223K50PS	0805C00560K20PS	0805C05101K50PS	0805C06220K50PS	080500647145005	CHROSTA156KW	VC2A2HUS4A4A	30028	30040	1464614	T.A.H. TC1165H	T.A. 4. CA3150H	FMS40599				FMS40616	DUPONT 8956	P6100	34030409	FPS18032
PARTS LIST	CONTRACT	FIFD OTY		- œ - ~	w 4	4	5 1	9	, ,	2	6	10 1	11	12 1	13 1	14 1	15 AR				17 AR	18 AR		REF REF	REF REF

END OF REPORT

1 SHEET

LAST SHEET OF

SINCGARS DISCRIMINATOR PARTS LIST

FIGURE 6-9.

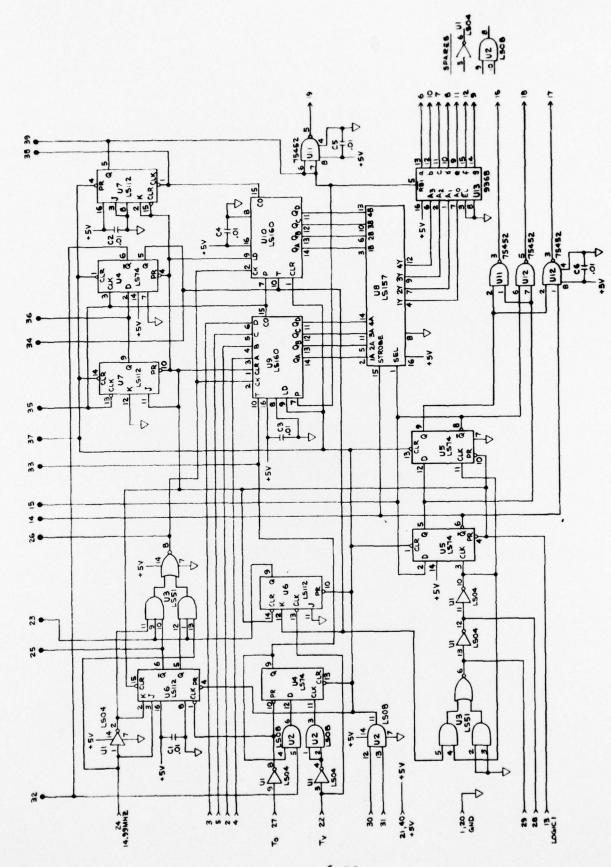


FIGURE 6-10. MINILASER COUNTER SCHEMATIC

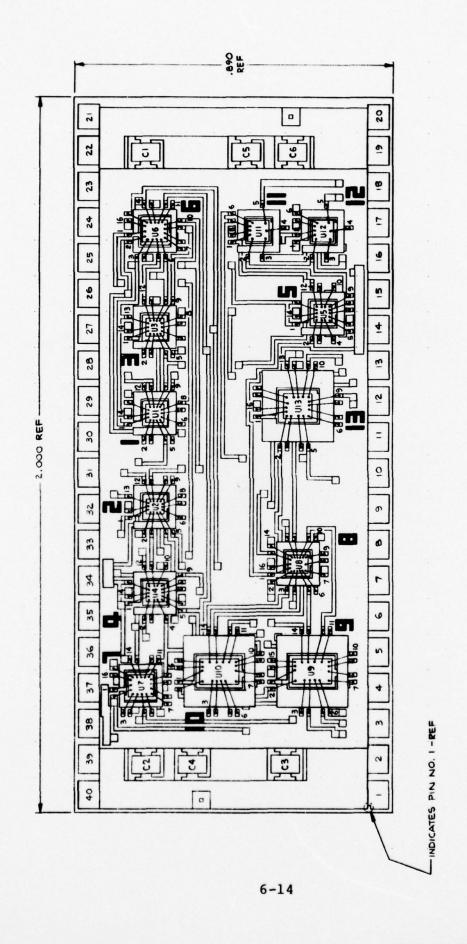


FIGURE 6-11. MINILASER CIRCUIT LAYOUT

REV AUTH NO. SHEET	CODE CODE CODE REMARKS IDENT	MIN REVISION -		VICLAN CI-CS	NAT UZ	NAT U3	NAT U4.U5	NAT U6.U7	NAT UB	NAT U9,U10	FAIR UII OIZ	FAIR UI3	TY II + ABLEBOND 36-2	62-36-2	DOW CORNING	.09 REF HIGH CHAR. 09128	METH HS AND/OR 82
ELL CODE IVENT IVISION 09128 PL34030411-001 G FLOPIDA LIST TITLE HYS ASSY HIMITASED COUNT	DESCRIPTION	_	INTLASER COUNTER	1.8.8. 1.C.	1.00	T.A.B. I.C.	1.6.	T.A.B. I.C.	T.A.B. I.C.	T.A.B. I.C.	T.A.B. I.C.		U.EPOXY ADH.				
OMEYW ICS D RSBUR	PART NUMBER	34030411	34030410-001	1805W5/103K50PS	T.A.R. 54L508	T.A.R. 54LS51	T.A.B. 541.574	T.A.3. 54LS112	T.A.E. 54L5157	T.A.3. 54LS160		T.A.B. 9368	FMS40599	9193NT 8956	P6100	FPS18032	
AVION ST. PETE	710	REF		• -	1	-	7	2	-	2	2	-	AR	AR	AP	REF	
PARTS LIST	FILID		(vm	*	5	•	1	œ	0	01	=	12	13	.14	REF	

FIGURE 6-12. MINILASER PARTS LIST

END OF REPORT

LAST SHEET OF 1 SHEET

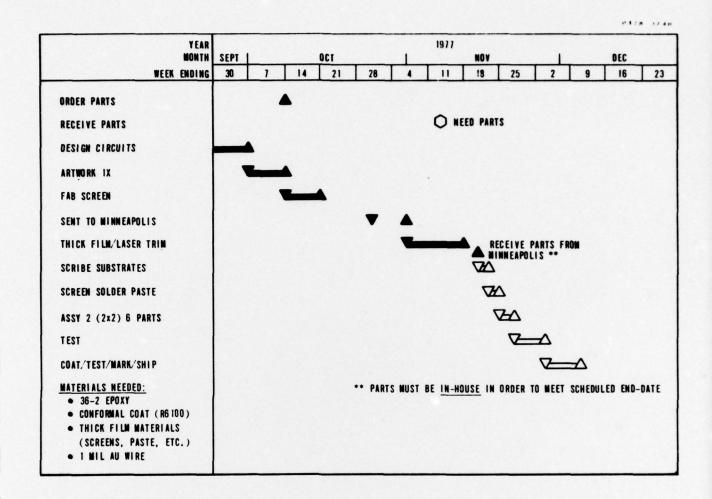


FIGURE 6-13. ELECTRONIC COMMUTATOR SCHEDULE



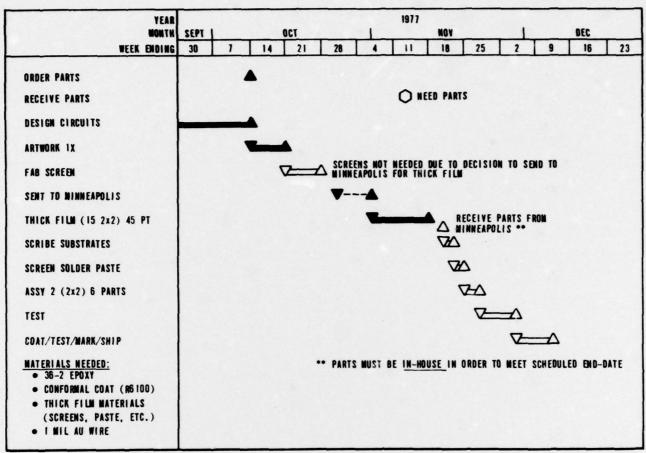


FIGURE 6-14. RAM SCHEDULE

the fall of the same

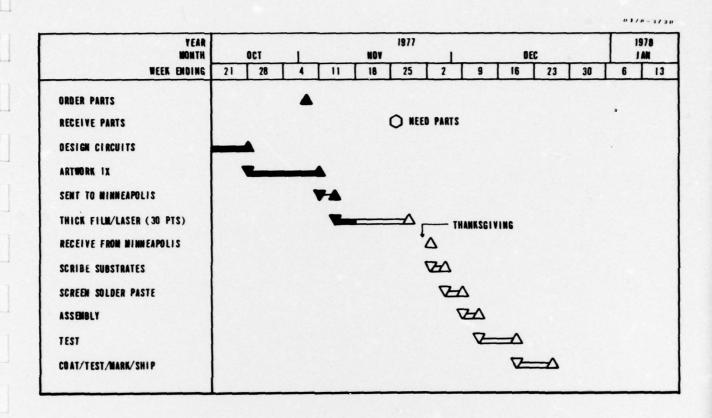


FIGURE 6-15. SINCGARS DISCRIMINATOR SCHEDULE

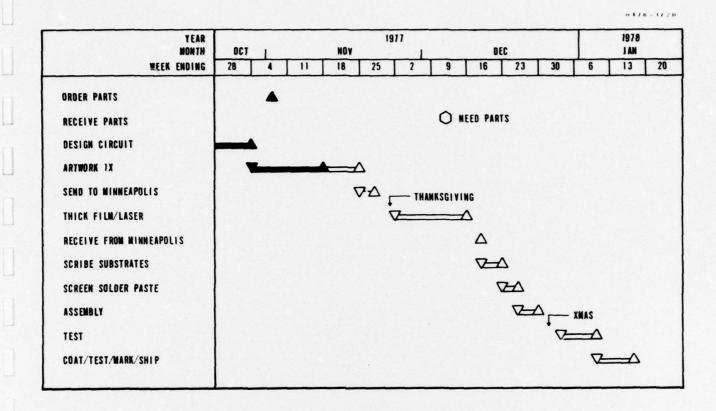


FIGURE 6-16. MINILASER COUNTER SCHEDULE

CONCLUSIONS

During this first reporting period excellent progress was made in all areas of endeavor. The equipment definition and specification work is proceeding on schedule with no major problems foreseen at this time. The facility requirements are being reviewed for more detailed determination during the next reporting period. material handling system design is ahead of schedule with good progress in two parallel approaches: the mechanical feed system and the air feed system. Both designs show so much promise that neither has been dropped at this time. Conventional process development is on schedule and should be largely completed by the close of the next reporting period. TCC process development is progressing on schedule with the definition of standard OLB Patterns and lead forming configurations. The design, layout and manufacture of engineering samples is progressing close to schedule, with the first four circuits expected to be delivered before or on 31 December 1977. last two devices will see delivery in January 1978. Significant design effort was expended on all six device types and satisfactory technical approaches have been identified. The overall program is close to schedule.

PLANS FOR NEXT REPORTING PERIOD

During the next reporting period engineering, design and layout activity of the six Engineering Sample devices should have been completed. At least four of the circuits are scheduled to be delivered with the last two to follow shortly. The mechanical and air feed material handling system design will have been completed. The first prototype of the mechanical feed system, adaptable to the Weltek printhead will be nearing completion. Most major equipment specifications will have been written, and appropriations to order will have been prepared. Detail design of the burn-in equipment will have been initiated.

PUBLICATIONS

- (1) "Advances in TAB for Hybrids TC OUter Lead Bonding". William R. Rodrigues de Miranda and Dr. R. G. Oswald
- (2) "Wafer Bumping for Tape Automated Bonding". James M. Montante, William R. Rodrigues de Miranda and Dr. Rudolph G. Oswald

Both papers were presented at the 1977 ISHM International Symposium in Baltimore, MD, October 24, 1977.

DISTRIBUTION LIST

Commander Defense Documentation Center Attn: DDC-TCA Cameron Station, Building 5 Alexandria, VA 22314	12	Commander US Army Research Office Attn: DRXRO-IP PO Box 12211 Research Triangle Park, NC 27709	1
Commander US Army Material Development and Readiness Command Attn: DRCRD-RS-PE-E, DRCRE-D 5001 Eisenhower Avenue	2	Commander US Army Electronics Command Attn: DRSEL-RD-PC Fort Monmouth, NJ 07703	2
Alexandria, VA 22333 Office of Defense Research & Engineering	1	Commander US Army Missile Command Redstone Scientific Info Center Attn: Chief, Document Section	1
Communications and Electronics Room 3D1037		Redstone Arsenal, AL 35809	
Washington, DC 20330 Director		US Army Missile Command Attn: DRSMI-RGP (Mr. Victor Ruwe) Redstone Arsenal, AL 35805	1
US Army Production Equipment Agency Attn: Mr. C. McBurney Rock Island Arsenal Rock Island, IL 61201	1	Commander Harry Diamond Laboratories Attn: Mr. Horst Gerlach (DRXDO-RAA) 2800 Powder Mill Road	1
Department of Defense Deputy for Science & Technology	1	Adelphia, MD 20783	
Ofc Assist Sec Army (R&D) Washington, DC 20310		Director Electronic Components Laboratory Attn: DRSEL-TL-IC	2
Advisory Group on Electron Devices 201 Varick Street, 9th Floor New York, NY 10014	2	Fort Monmouth, NJ 07703 Commander	1
	1	Naval Electronics Laboratory Center	
Chief, Research & Development Attn: Director of Developments Department of the Army	•	Attn: Library San Diego, CA 92152	
Washington, DC 20310		Commander Department of the Navy, Elex 05143A	1
Director Electronic Components Laboratory Attn: DRSEL-TL-IJ Fort Monmouth, NJ 07703	1	Attn: A. H. Young Electronics System Command Washington, DC 20360	

DRSEL-RD-PC	
Distribution	List

Chief Naval Ship Systems Command Department of the Navy Attn: Code 681A2b, Mr. L. Gumina Room 3329	1	NASA Scientific & Tech Information Facility PO Box 8757 Baltimore/Washington Int'l Airport	2
Washington, DC Commander US Naval Air Development Center Attn: Library Johnsville, Warminster, PA 18974	2	National Aeronautics & Space Administration George C. Marshall Space Flight Center Attn: R-QUAL-FP (Mr. Leon C. Hamiter) Huntsville, AL 35812	1
Commander Naval Ocean Systems Command Attn: Mr. Richard Gamble San Diego, CA 92152	1	NSSA-Manned Space Craft Center Reliability and Flight Safety Division Attn: Library	1
Commander Air Research & Development Command Attn: RDTCT Andrews Air Force Base Washington, DC	1	Houston, TX Alpha Industries, Inc. 20 Sylvan Road Woburn, MA 08101	1
Commander Rome Air Development Center Griffiss Air Force Base Attn: (EMERR) Mr. L. Gubbins Rome, NY 13440	1	AVANTEK Attn: Mr. Thielan 3001 Copper Road Santa Clara, CA 95051	1
Commander Air Force Materials Laboratory Attn: MATE (Ms. E. Torrance)	1	Amelco Semiconductor PO Box 1030 Mountain View, CA 94042	1
Electronic Branch Wright-Patterson Air Force Base Dayton, Ohio 45433		Bell Laboratories Whippany Road Attn: Tech Reports Center WH5E-227	1
Commander Rome Air Development Center Griffiss Air Force Base Attn: (EMERR) Mr. Regis C. Hilow Rome, NY 13440	1	Whippany, NJ 07981 Centralab Semiconductor 4501 North Arden Drive El Monte, CA 91734	1
AFAL (AVTA) Electronic Technology Division Attn: Mr. Robert D. Larson, Chief Advanced Electronics Devices Branch Wright-Patterson Air Force Base Dayton, Ohio 45433	1	Philco Ford Attn: Mr. D. Heiden Landsdale, PA	1

DRSEL-RD-PC	
Distribution	List

General Instrument Corp. Semiconductor Products Group 600 West John Street Hicksville, Long Island, NY 11802	1	Motorola, Inc. Attn: Mr. J. LaRue 5005 East McDowell Road Phoenix, AZ 85008	1
Hughes Aircraft Corporation Attn: Dr. Bruce Gammer 3100 West Lmita Boulevard Torrance, CA 90509	1	Dr. Robert H. Rediker Massachusetts Institute of Technology Building 13-3050 Cambridge, MA 02139	1
Hughes Aircraft Corporation Attn: Mr. D. Hartman Bldg. 100 MSA788 PO Box 90515 Los Angeles, CA 90009	1	Microwave Associates Attn: Dr. F. Brand Burlington, MA 01803	1
Hughes Aircraft Company Microelectronics Laboratory 500 Superior Avenue Newport Beach, CA 92663	1	Rockwell International Collins Radio Group Attn: Dr. Cheng Wen MS 406-246 Dallas, TX 75207	1
Honeywell Inc. Semiconductor Products 2747 Fourth Avenue Minneapolis, MN 55408	1	Northrop Corporation Laboratories Attn: Library 320-61 3401 West Broadway Hawthorne, CA 90250	1
Harris Semiconductor PO Box 883 Melbourne, FL 32901	1	Rockwell International Corporation Science Center Thousand Oaks, CA	1
IBM Components Division Attn: Mr. Al Kran East Fishkill, Rt. 52 Hopewell Junction, NY 12533	1	Raytheon Company Semiconductor Operation Attn: Mr. S. Weinsner 350 Ellis Street Mountain View, CA 94040	1
KSC Semiconductor Corp Attn: Mr. S. Cudletz, President KSC Way (Katrina Road) Chelmsford, MA 01824	1	Raytheon Company Attn: Dr. S. F. Paik 130 Second Avenue Waltham, MA 02154	1
Arthur D. Little Acorn Park Attn: Dr. H. Rudenberg 15/206 Cambridge, MA 02140	1		

DRSEL-RD-PC Distribution List

Sanders Associates, Inc. Attn: Microwave Department 95 Canal Street Nashua, NH 03060

TRW Semiconductor Inc. Attn: Mr. B. Lindgren 14520 Aviation Blvd Lawndale, CA 90260 1 Texas Instruments
Attn: Technical Reports Service
PO Box 5936, MS 105
Dallas, TX 75222

1

1

1 Varian Associates
Solid State West
Attn: F. E. Rosztoczy
611 Hansen Way
Palo Alto, CA 94303